

PRIOR ART Fig. 1



MV Interface Description
[This interface uses additional pins to provide Tag/mode selection info for future implementations of high latency RDC designs with latency could be more than one sector long

MCMD timing chart for Write/Read operations

Media Gate MCMD [1:0] Tgsetup -Tmhold Tmsetup



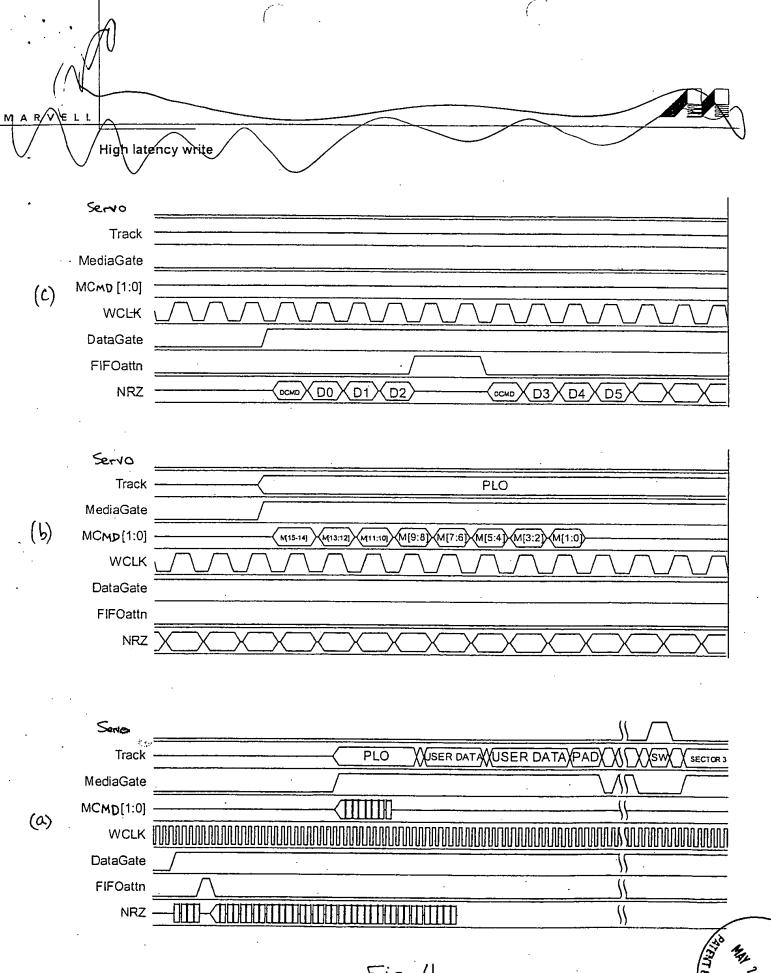


Fig. 4